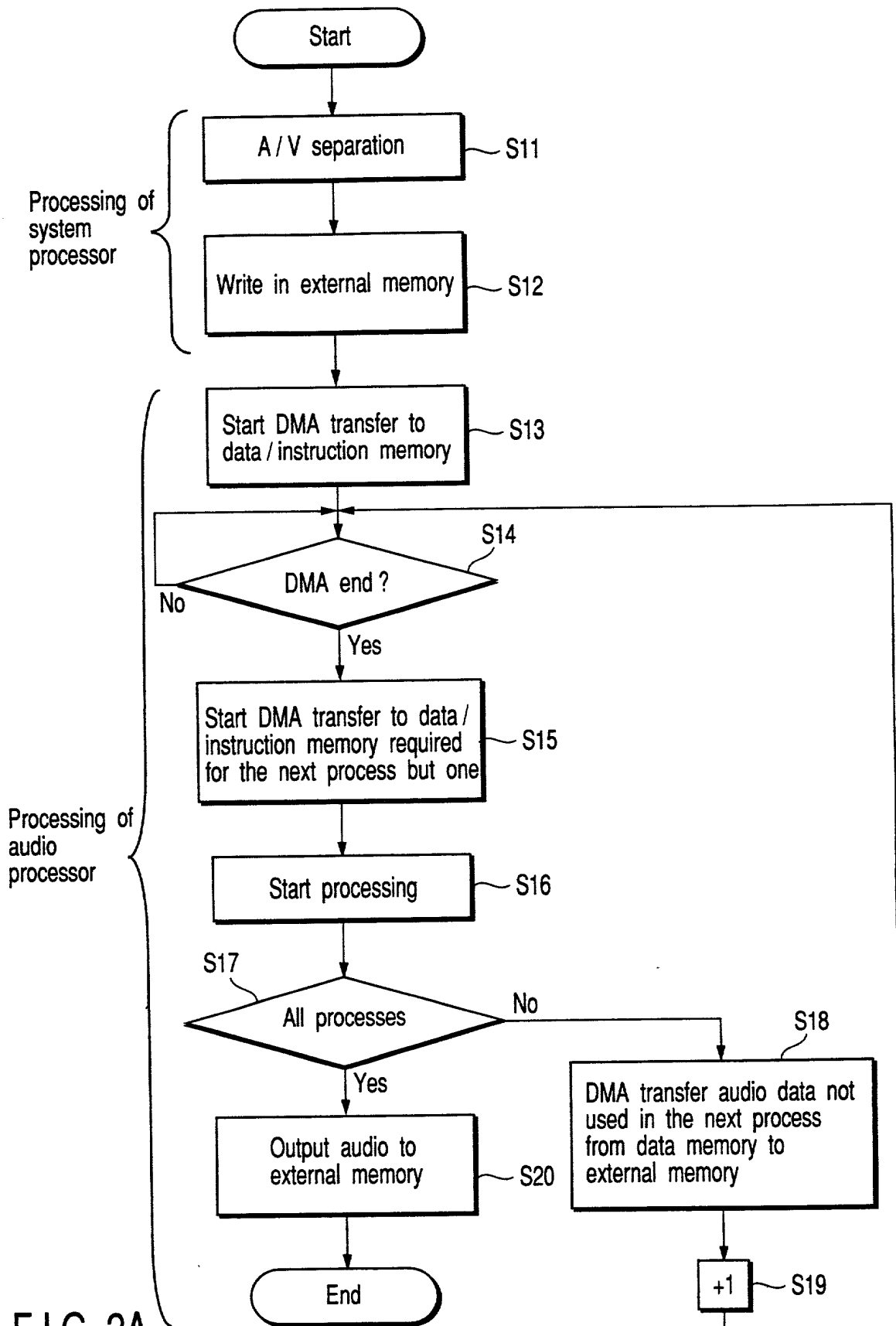


FIG. 1



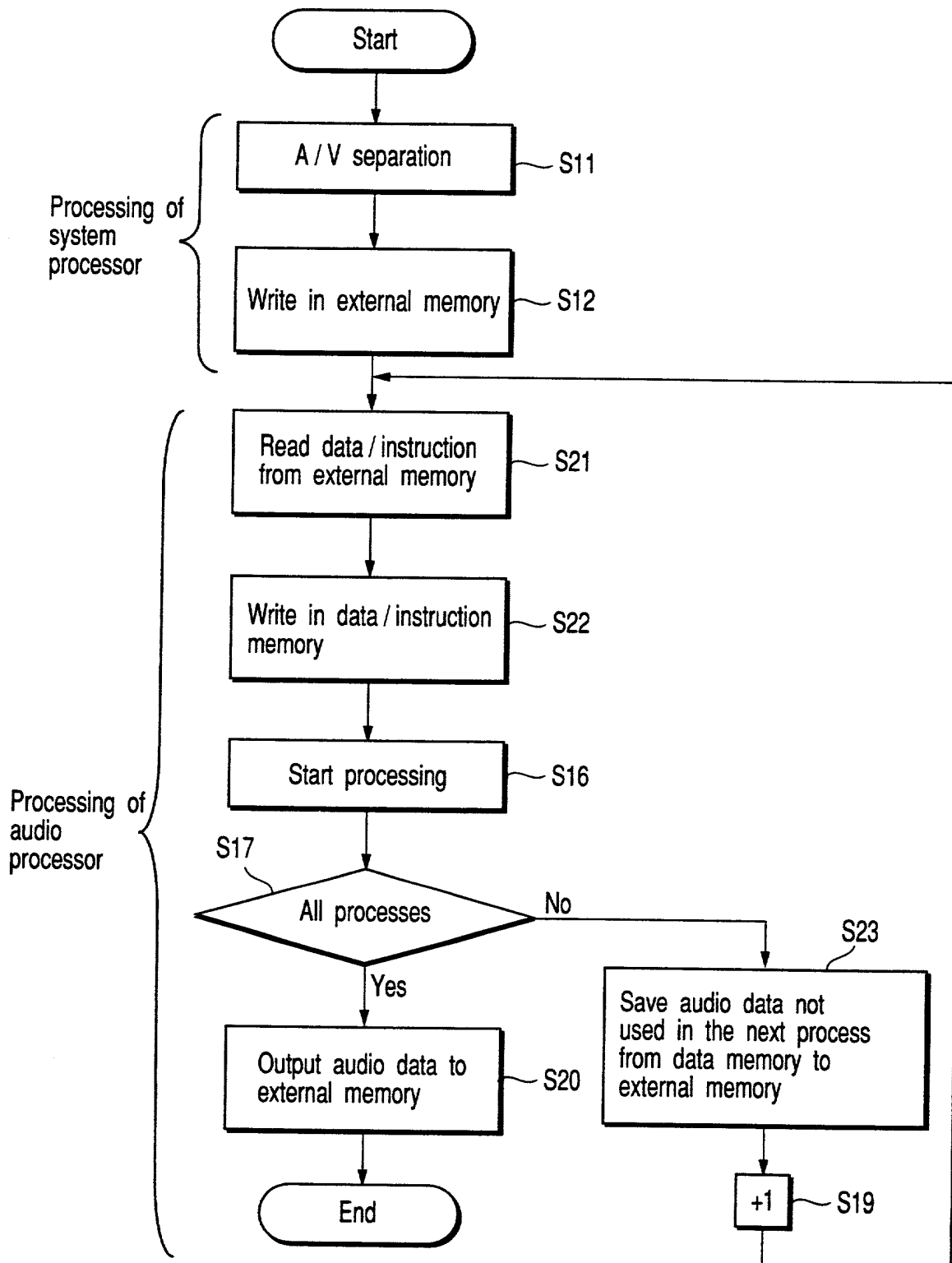


FIG. 2B

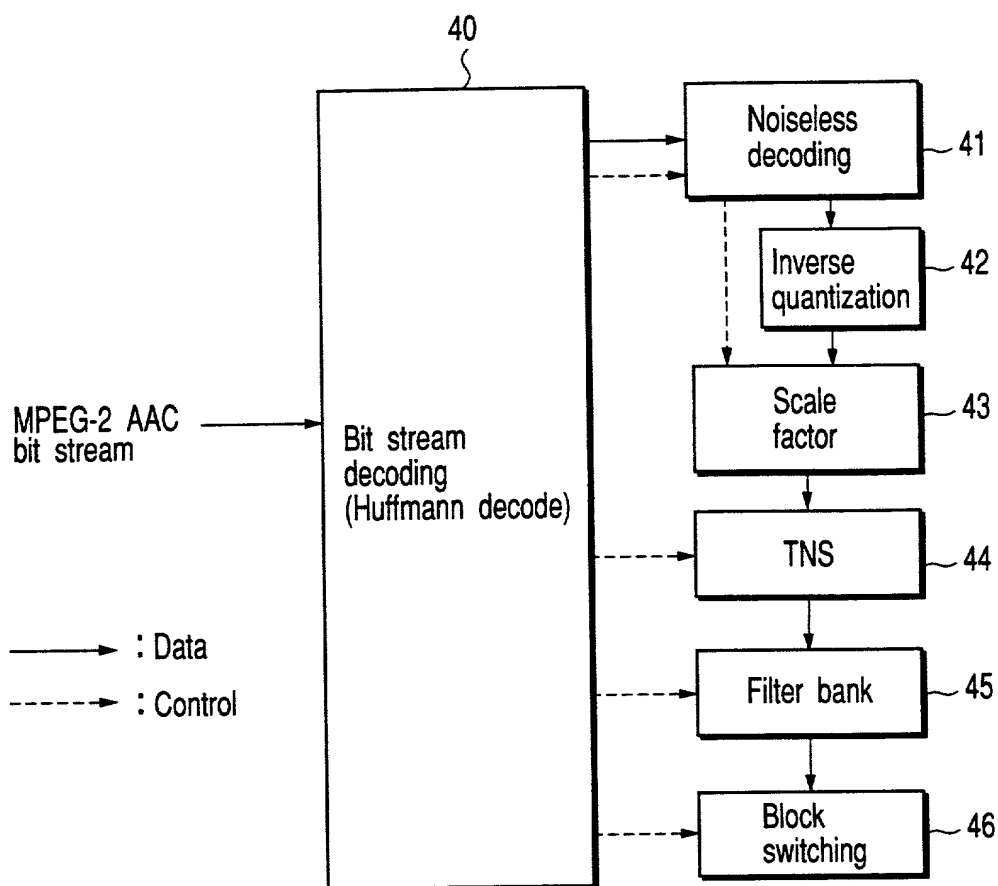
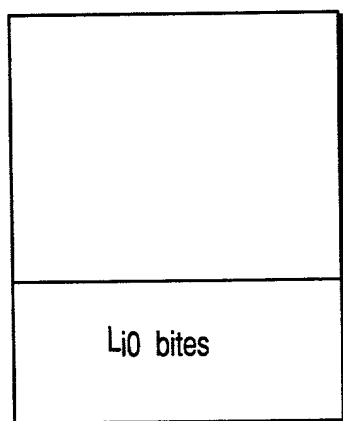
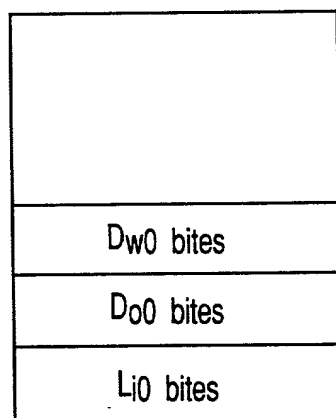


FIG. 3



Instruction memory

FIG. 4A



Data memory

FIG. 4B

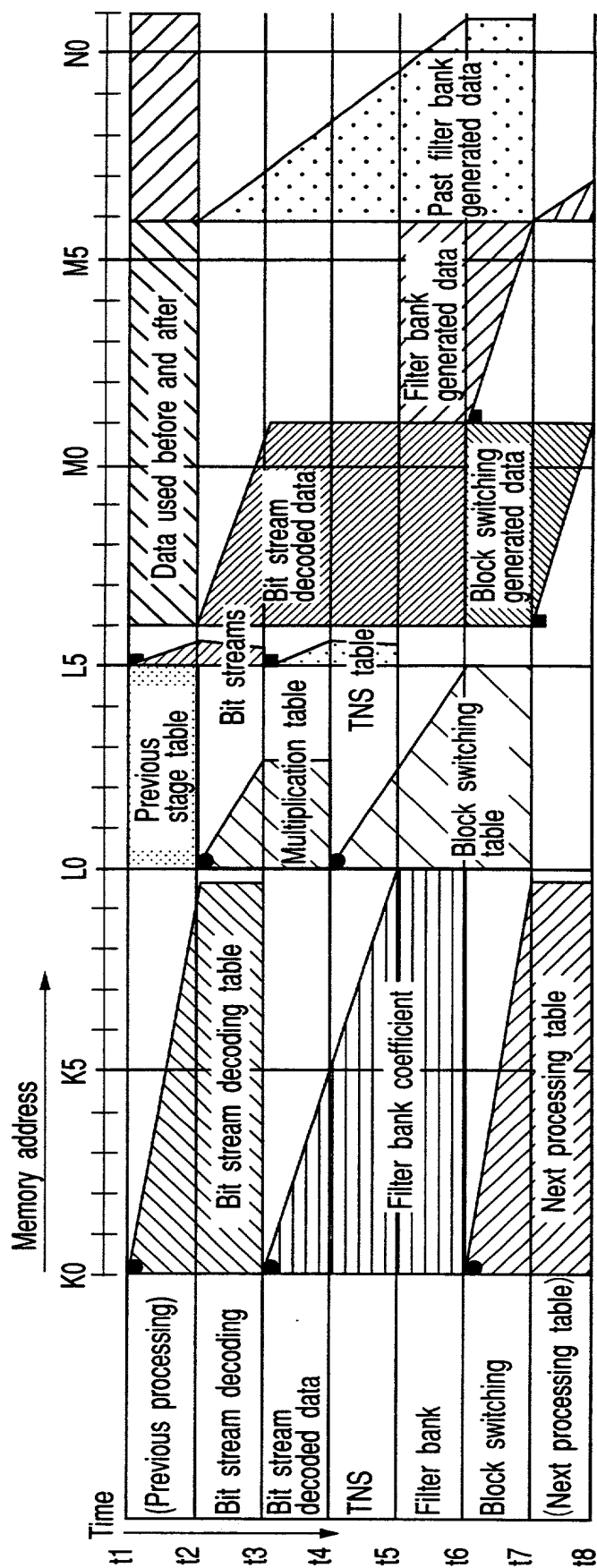


FIG.5

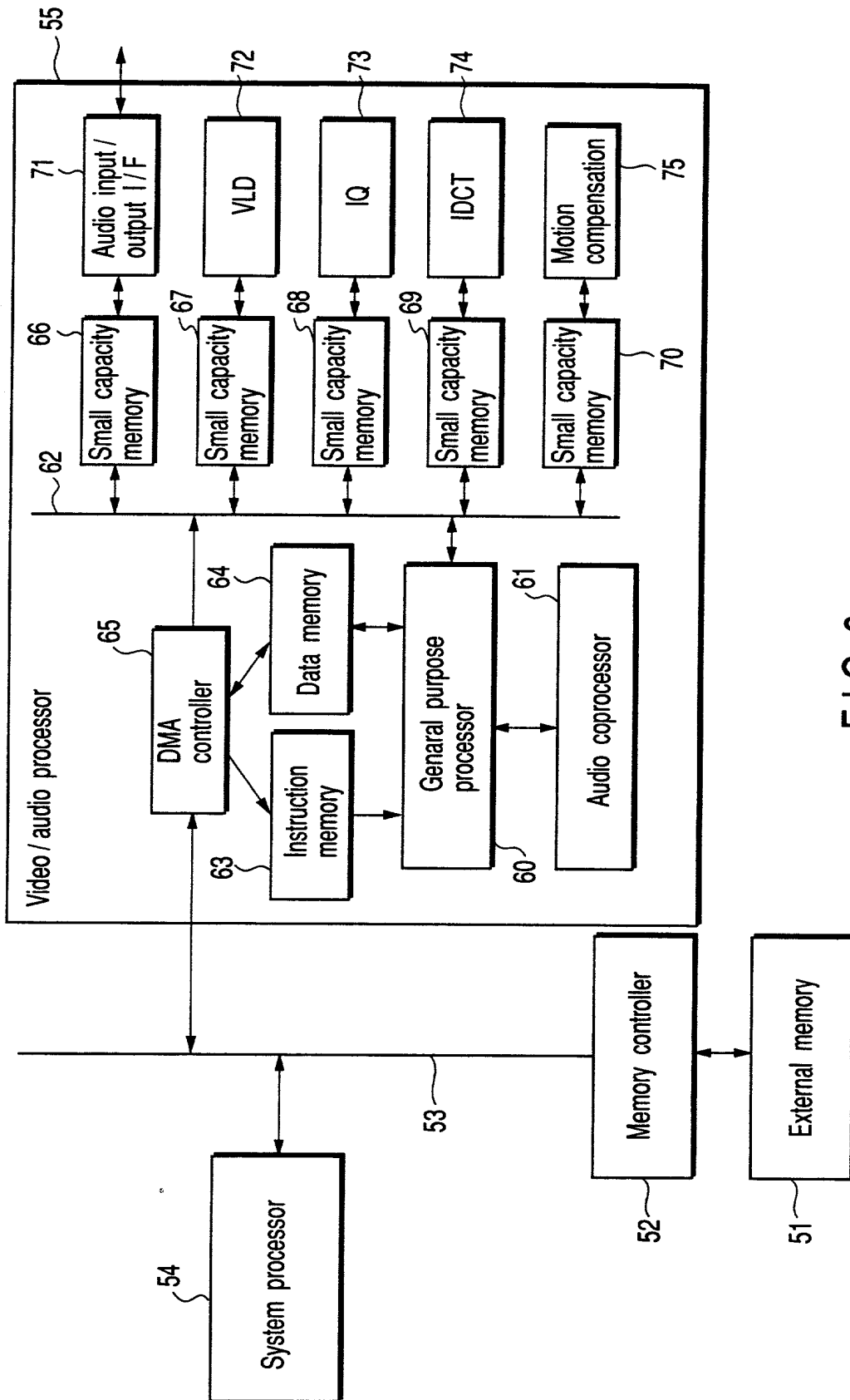


FIG. 6